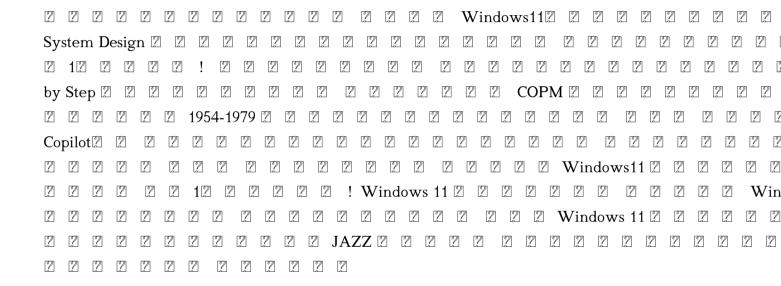
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2013-11-21

### 2016-02-01

perhaps nothing characterizes the inherent heterogeneity in embedded sys tems than the ability to choose between hardware and software implementations of a given system function indeed most embedded systems at their core repre sent a careful division and design of hardware and software parts of the system to do this task effectively models and methods are necessary functionality to capture application behavior needs and system implementation constraints formal modeling can be valuable in addressing these tasks as with most engineering domains co design practice defines the state of the it seeks to add new capabilities in system conceptualization mod art

though eling optimization and implementation these advances particularly those related to synthesis and verification tasks directly depend upon formal under standing of system behavior and performance measures current practice in system modeling relies upon exploiting high level programming frameworks such as systems esterei to capture design at increasingly higher levels of ab straction and attempts to reduce the system implementation task while raising the abstraction levels for design and verification tasks to be really useful these approaches must also provide for reuse adaptation of the existing intellectual property ip blocks

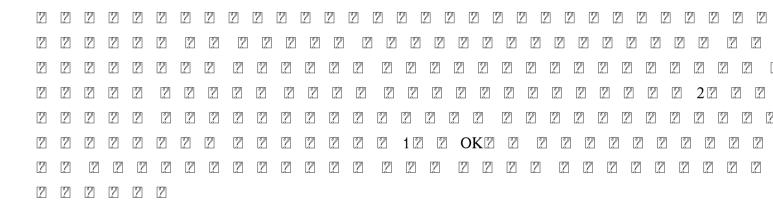
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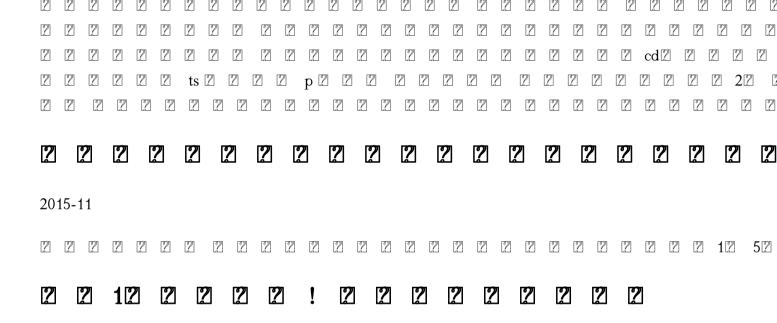
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## Formal Methods and Models for System Design

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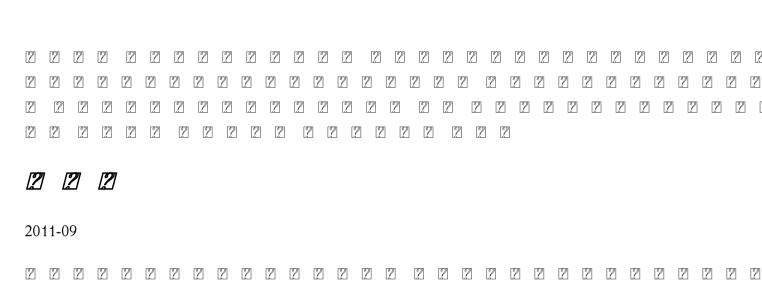
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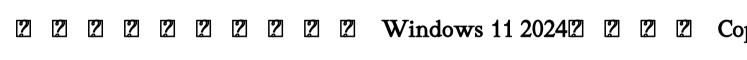
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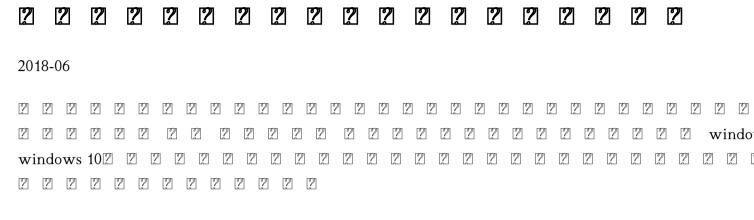
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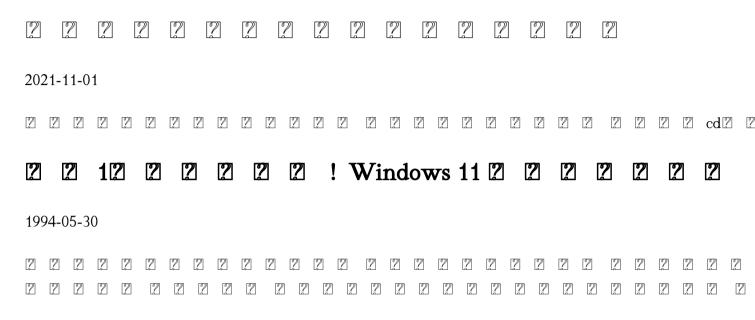
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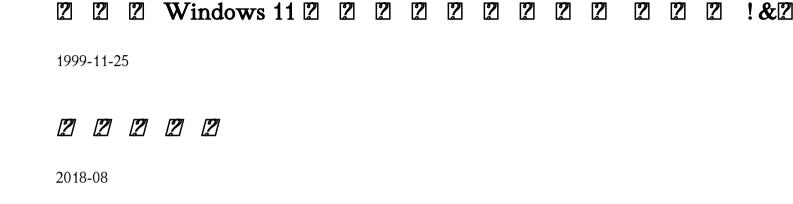
1994

a toy bear in a department store wants a number of things but when a little girl finally buys him he finds what he has always wanted most of all

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1991

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2001-10-25

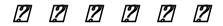


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2008-02-29



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